Amendments to the Claims:

1. (Currently Amended) A server configured to receive a request for an Open Library Architecture Delay and Power Calculation Module and produce the Open Library Architecture Delay and Power Calculation Module in response to the request, said server configured to create a Delay Calculation Language memory module based on the request, ~~and~~ compile the Delay Calculation Language memory module into the Open Library Architecture Delay and Power Calculation Module, wherein DCL/DPCM data for a specific memory configuration is created, and invoke a compiler to compile C-source to create the Open Library Architecture Delay and Power Calculation Module, wherein Delay Calculation Language side files are created which are used during the compilation with information on relevant include files and calculation files.

2. (Original) A server as defined in claim 1, wherein said server is configured to compile the Delay Calculation Language memory module into an intermediate form, and is configured to compile the intermediate form into the Open Library Architecture Delay and Power Calculation Module.

3. (Original) A server as defined in claim 2, wherein the intermediate form is C-source.

4. (Original) A server as defined in claim 1, wherein the server is configured such that the Open Library Architecture Delay and Power Calculation Module is downloadable.

5. (Original) A server as defined in claim 1, wherein the server is configured to receive a request which specifies the configurations and types of memories for which an Open Library Architecture Delay and Power Calculation Module is needed.

6. (Original) A server as defined in claim 1, further comprising Common Gateway Interface/Practical Extraction and Report Language Script which is configured to process the request.

7. (Original) A server as defined in claim 6, wherein the Common Gateway Interface/Practical Extraction and Report Language Script is configured to process the request by sourcing necessary environment variables, running a memory generation tool to create Delay Calculation Language memory modules, invoking a compiler to compile the Delay Calculation Language memory modules.

8. (Original) A server as defined in claim 7, wherein the Common Gateway Interface/Practical Extraction and Report Language Script is configured to invoke a first compiler to compile the Delay Calculation Language memory modules into C-source, and invoking a second compiler to compile the C-source to create the Open Library Architecture Delay and Power Calculation Module.

9. (Original) A server as defined in claim 7, wherein the Common Gateway Interface/Practical Extraction and Report Language Script is configured to create Delay Calculation Language side files which are used during the compilation with information on relevant include files and calculation files.

10. (Original) A web based Open Library Architecture memory generator configured to receive a request for an Open Library Architecture Delay and Power Calculation Module and produce the Open Library Architecture Delay and Power Calculation Module in response to the request, wherein the request specifies the configurations and types of memories for which an Open Library Architecture Delay and Power Calculation Module is needed, said web based Open Library Architecture memory generator configured to create a Delay Calculation Language memory module based on the request, and compile the Delay Calculation Language memory module into the Open Library Architecture Delay and Power Calculation Module, wherein said web based Open Library Architecture memory generator is configured to compile the Delay Calculation Language memory module into C-source, and is configured to compile the C-source into the Open Library Architecture Delay and Power Calculation Module, wherein said web based Open Library Architecture memory generator is configured such that the Open Library Architecture Delay and Power Calculation Module is downloadable, said web based Open Library Architecture memory generator further comprising code configured to process the request by sourcing necessary environment variables, running a memory generation tool to create Delay Calculation Language memory modules, invoking a first compiler to compile the Delay

Calculation Language memory modules into C-source, and invoking a second compiler to compile the C-source to create the Open Library Architecture Delay and Power Calculation Module, wherein the code is configured to create Delay Calculation Language side files which are used during the compilation with information on relevant include files and calculation files.

11. (Currently Amended) A user interface for generating and submitting a request for an Open Library Architecture Delay and Power Calculation Module, wherein the request specifies a configuration of memories for which the Open Library Architecture Delay and Power Calculation Module is needed, said user interface comprising a memory generation tool useable to create a Delay Calculation Language model, said user interface configured to generate the request based on the Delay Calculation Language model, wherein DCL/DPCM data for a specific memory configuration is created, and said user interface configured to invoke a compiler to compile C-source to create the Open Library Architecture Delay and Power Calculation Module, wherein Delay Calculation Language side files are created which are used during the compilation with information on relevant include files and calculation files.

12. (Original) A user interface as defined in claim 11, further comprising a library of templates which the memory generation tool uses to create the Delay Calculation Language model.

13. (Original) A user interface as defined in claim 11, wherein the user interface is configured to create a Hyper Text Markup Language file based on the Delay Calculation Language model.

14. (Original) A user interface as defined in claim 13, wherein said Hyper Text Markup Language file is configured to provide selectability of memory configurations and types.

15. (Original) A user interface as defined in claim 11, wherein the user interface is configured such that an Open Library Architecture Delay and Power Calculation Module based on the request is downloadable.

16. (Currently Amended) A method of obtaining an Open Library Architecture Delay and Power Calculation Module, said method comprising employing a user interface to generate a request for the Open Library Architecture Delay and Power Calculation Module, sending the request to a server, having the server generate the Open Library Architecture Delay and Power Calculation Module, ~~and~~ downloading from the server the Open Library Architecture Delay and Power Calculation Module which has been generated by the server, wherein DCL/DPCM data for a specific memory configuration is created, and invoking a compiler to compile C-source to create the Open Library Architecture Delay and Power Calculation Module, wherein Delay Calculation Language side files are created which are used during the compilation with information on relevant include files and calculation files.

17. (Original) A method as defined in claim 16, wherein the request specifies the configurations and types of memories for which an Open Library Architecture Delay and Power Calculation Module is needed.

18. (Original) A method as defined in claim 16, further comprising using a memory generation tool to create a Delay Calculation Language model, and having the user interface generate the request based on the Delay Calculation Language model.

19. (Original) A method as defined in claim 16, further comprising using a library of templates to create the Delay Calculation Language model.

20. (Currently Amended) A system for generating an Open Library Architecture Delay and Power Calculation Module, said system comprising: a user interface for generating and submitting a request for the Open Library Architecture Delay and Power Calculation Module, wherein the request specifies a configuration of memories for which the Open Library Architecture Delay and Power Calculation Module is needed; and a server configured to receive the request for the Open Library Architecture Delay and Power Calculation Module from the user interface and produce the Open Library Architecture Delay and Power Calculation Module in response to the request, wherein DCL/DPCM data for a specific memory configuration is created; and invoke a compiler to compile C-source to create the Open Library Architecture Delay and Power Calculation Module, wherein Delay Calculation Language side files are created which are used during the compilation with information on relevant include files and calculation files.